# Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications 18.1 for Intel Core<sup>™</sup> and Intel Xeon<sup>®</sup> Processors Release Notes

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Intel® CPU Runtime for OpenCL™ Applications 18.1 - Release Notes

## **1** Introduction

The Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications 18.1 (previously known as Intel<sup>®</sup> SDK for OpenCL<sup>™</sup> - CPU only Runtime Package) provides OpenCL<sup>™</sup> support for Intel<sup>®</sup> CPU devices.

More runtimes and drivers with support for Intel<sup>®</sup> Graphics Technology and the Intel Xeon Phi<sup>™</sup> coprocessor are available at <u>https://software.intel.com/en-us/articles/opencl-drivers</u>.

The Intel<sup>®</sup> CPU Runtime for OpenCL<sup>m</sup> Applications is fully compatible with Intel<sup>®</sup> SDK for OpenCL<sup>m</sup> Applications – a complete development suite for developing, debugging, and profiling OpenCL<sup>m</sup> applications. For more information, see <u>https://software.intel.com/enus/intel-opencl</u>.

This document contains system requirements, installation instructions, information about known issues and limitations, and legal information.

#### **Customer support**

To learn more about this product, see documentation, FAQ, code samples, and other support information at this site: <u>https://software.intel.com/en-us/intel-opencl-support</u>.

For technical support, including answers to questions not addressed in the installed product, go to the OpenCL<sup>™</sup> forum at this site: <u>https://software.intel.com/en-us/forums/opencl</u>.

Refer to <u>https://software.intel.com/en-us/iocl\_tec\_opg</u> for OpenCL<sup>™</sup> Developer Guide for Intel<sup>®</sup> Core<sup>™</sup> and Intel<sup>®</sup> Xeon<sup>®</sup> Processor.

## 2 What's New

The 18.1 release includes:

- Support of Intel<sup>®</sup> Advanced Vector Extensions 512 (Intel<sup>®</sup> AVX-512) ISA on Intel<sup>®</sup> Xeon<sup>®</sup> Platinum processor (formerly code name Skylake).
- Enabled features of OpenCL<sup>™</sup> 2.1. The product is based on a published Khronos\* Specification and has passed the Khronos Conformance Process. The conformance record can be found at <u>https://www.khronos.org/conformance/adopters/conformantproducts/opencl</u>. Refer to submission #322 recorded on October 7, 2018.

- Support for vectorization width 16 for the environment and configuration file variable CL\_CONFIG\_CPU\_VECTORIZER\_MODE, as well as for OpenCL<sup>™</sup> C optional kernel attribute intel\_vec\_len\_hint
- Support for OpenCL<sup>™</sup> Kernel debugging on Linux\* OS with GDB\*
- Improved coexistence support with Intel<sup>®</sup> Graphics Compute Runtime for OpenCL<sup>™</sup> Driver when both are installed
- Changed the platform name returned via clGetPlatformInfo OpenCL<sup>™</sup> API call with CL PLATFORM NAME bitflag to "Intel(R) CPU Runtime for OpenCL(TM) Applications"
- New environment variable CL\_CONFIG\_CPU\_TARGET\_ARCH

It generates code exclusively for a given target CPU architecture. Allows only lowering the instruction set level supported by CPU

#### Allowed values are:

skx	Generates code for processors that support Intel® Advanced Vector Extensions 512 (Intel® AVX-512) Foundation instructions, Intel® AVX- 512 Conflict Detection instructions, Intel® AVX-512 Doubleword and Quadword instructions, Intel® AVX-512 Byte and Word instructions and Intel® AVX-512 Vector Length Extensions for Intel® processors, and the instructions enabled with core-avx2.
core-avx2	Generates code for processors that support Intel® Advanced Vector Extensions 2 (Intel® AVX2), Intel® AVX, SSE4.2 SSE4.1, SSE3, SSE2, SSE, and SSSE3 instructions.
corei7-avx	Generates code for processors that support Intel® Advanced Vector Extensions (Intel® AVX), Intel® SSE4.2, SSE4.1, SSE3, SSE2, SSE, and SSSE3 instructions.
corei7	Generates code for processors that support Intel® SSE4.2 Efficient Accelerated String and Text Processing instructions. May also generate code for Intel® SSE4 Vectorizing Compiler and Media Accelerator, Intel® SSE3, SSE2, SSE, and SSSE3 instructions.

• Fixed an issue with user functions not being inlined in programs created using clCreateProgramWithIL OpenCL<sup>™</sup> API call

- Fixed incorrectly reported CL\_DEVICE\_MAX\_COMPUTE\_UNITS for multi-socket Intel<sup>®</sup> Xeon<sup>™</sup> systems (reported on forum <u>https://software.intel.com/en-us/forums/opencl/topic/702240</u>)
- Fixed incompatibility with Intel<sup>®</sup> Threading Building Blocks (Intel<sup>®</sup> TBB) <u>max\_allowed\_parallelism parameter</u>
- Fixed an issue with CL\_DRIVER\_VERSION returning incorrect driver version
- Improved OpenCL<sup>™</sup> C compiler diagnostics
- Minor bug fixes
- Updated the compiler infrastructure to LLVM\* version 6.0

**NOTE:** Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications 18.1 supports CPU only. For Intel Xeon Phi<sup>™</sup> coprocessor support, use the version 14.2. For more information, see OpenCL<sup>™</sup> runtime entry and release notes on the OpenCL<sup>™</sup> driver page at: <u>https://software.intel.com/en-us/articles/opencl-drivers</u>.

## **3** System Requirements

For an explanation of architecture names, see <u>http://software.intel.com/en-us/articles/intel-architecture-platform-terminology/</u>.

#### 3.2.1 **Processor Requirements**

The Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications 18.1 provides CPU device support on the following processors:

- Intel Core<sup>™</sup> processor family with Intel<sup>®</sup> Streaming SIMD Extensions 4.2 (Intel<sup>®</sup> SSE4.2) support or higher
- Intel Xeon® processor E3, E5, and E7 families with Intel® SSE4.2 support or higher
- Intel Xeon<sup>®</sup> Scalable processors Platinum, Gold, Silver, Bronze families with Intel<sup>®</sup> SSE4.2 support or higher

Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications 18.1 provides optimizations for processors that support the following instruction sets:

Intel<sup>®</sup> Advanced Vector Extensions 512 (Intel<sup>®</sup> AVX-512) Foundation instructions (Intel<sup>®</sup> AVX-512F), Intel<sup>®</sup> AVX-512 Conflict Detection instructions (AVX-512CD), Intel<sup>®</sup> AVX-512 Doubleword and Quadword instructions (AVX-512DQ), Intel<sup>®</sup> AVX-512 Byte and

Word instructions (AVX-512BW) and Intel® AVX-512 Vector Length Extensions (AVX-512VL)

- Intel<sup>®</sup> Advanced Vector Extensions 2 (Intel<sup>®</sup> AVX2)
- Intel<sup>®</sup> Advanced Vector Extensions (Intel<sup>®</sup> AVX)
- Intel<sup>®</sup> Streaming SIMD Extensions 4.2 (Intel<sup>®</sup> SSE4.2)

To enable Intel<sup>®</sup> Graphics Technology device support on the aforementioned processors on Windows\*, install the Intel<sup>®</sup> Graphics Driver package. It includes the Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications. For Intel<sup>®</sup> Graphics Technology support on Linux\* operating systems, refer to Intel<sup>®</sup> Graphics Compute Runtime for OpenCL<sup>™</sup> (<u>https://01.org/compute-runtime</u>).

**NOTE:** Incompatible or proprietary instructions in non-Intel processors may cause the analysis capabilities of this product to function incorrectly. Any attempt to analyze code not supported by Intel processors may lead to failures in this product.

#### 3.2.2 Software Requirements

#### **Supported Operating Systems**

The following is the list of supported operating systems:

Linux\* operating systems (64-bit only):

- Red Hat Enterprise Linux\* OS 7.4
- SUSE Linux Enterprise Server\* 12SP3
- Ubuntu\* 16.04
- Cent OS 7.4

Windows\* operating systems (32- and 64-bit):

- Microsoft Windows\* Server 2016
- Microsoft Windows\* 10

Higher versions are expected to be functional.

On Linux\* OS libnuma1, zlib1g, libxml2 packages are required.

For better OpenCL<sup>™</sup> Kernel CPU Debugging experience on Linux<sup>\*</sup> OS, GDB<sup>\*</sup> 7.12 or higher is required. In cases where an older GDB<sup>\*</sup> version is used, some issues related to JIT code debugging may occur (such as no automatic breakpoints reset between runs, fail to stop on a breakpoint for second and other runs).

## 4 Installation Notes

## 4.1 Installation on Microsoft Windows\*

To install the Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications on Windows\* systems, download the Runtime package and follow the installer prompts.

To remove the Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications, go to **Control Panel** > **Programs** and Features > **OpenCL<sup>™</sup> Runtime** > **Uninstall**.

#### 4.2.3 Installation on Linux\*

The following sections describe installing and uninstalling this product on Linux\* OS.

#### 4.2.4 Installing the Product

To install the product, do the following:

- 1. Extract the TGZ archive contents:
  - # tar xzf opencl\_runtime\_18.1\_x64\_<OS>\_<VERSION>.tgz
  - # cd opencl\_runtime\_18.1\_x64\_<OS>\_<VERSION>
- 2. Run the following command (for command-line interface) and follow the installer prompts:
  - # ./install.sh

Alternatively (for installation with graphical user interface), run the following command:

# ./install\_GUI.sh

#### 4.2.5 Uninstalling the Product

To uninstall the product using the uninstallation script, do the following:

1. Go to the folder with the Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications installation (for example, /opt/intel/opencl).

2. Run the uninstall.sh script.

You can use an operating system-specific command to remove all the packages starting with "intel-openclrt-18.1". To do so, run the following commands:

- For Red Hat Enterprise Linux OS:
- # sudo yum remove "intel-openclrt-18.1.\*"
- For SUSE Linux Enterprise OS:
- # sudo zypper remove "intel-openclrt-18.1.\*"
- For Ubuntu OS:
- # sudo apt-get purge "intel-openclrt-18.1.\*"

## 5 Installation and Configuration Issues

#### **PATH Environment Variable**

The Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications installer adds the target installation folder of the CPU runtime to the end of the system PATH environment variable. If the variable is too long, the application might not be able to load the CPU runtime DLL files. To solve this problem, move the folders to the beginning of the PATH variable or delete unnecessary folders from the PATH.

#### **Installation Conflict**

If the Intel<sup>®</sup> HD Graphics Driver is installed on your Windows\* system, you will not be able to install the Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications.

However, to update OpenCL<sup>™</sup> for CPU support, you can perform a manual installation of the package by performing the actions specified in section 5.1 (Windows\*) or 5.2 (Linux\*) of this document. Note that these steps performed on a system with Intel<sup>®</sup> HD Graphics Driver installed do not guarantee OpenCL<sup>™</sup> for Intel<sup>®</sup> HD Graphics functionality to remain stable.

#### 5.1 Manual Installation on Windows\* OS

- 1. Open a command prompt.
- 2. Download the Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications (MSI version)
- 3. Extract binaries from the MSI package:

msiexec /a opencl\_runtime\_18.1\_x64\_setup.msi /qn TARGETDIR=<TMP\_DIR>

4. Copy the content of <TMP\_DIR>\Intel\OpenCL to <INST\_DIR> of your choice.

- 5. Replace the OpenCL.dll files in C:\Windows\SysWOW64 and C:\Windows\System32 with files from <TMP\_DIR>\SystemFolder and <TMP\_DIR>\System64Folder directories correspondingly. If you want to restore the initial files, preserve them in a temporary location.
- 6. Add <INST\_DIR>\windows\compiler\lib\intel64\_win and <INST\_DIR>\windows\compiler\lib\ia32\_win to the PATH environment variable.
- Disable all Intel-related keys in the branches below by setting them to 1: [HKEY\_LOCAL\_MACHINE\SOFTWARE\Khronos\OpenCL\Vendors]
   [HKEY\_LOCAL\_MACHINE\SOFTWARE\WOW6432Node\Khronos\OpenCL\Vendors]
- Add registry key as follows: [HKEY\_LOCAL\_MACHINE\SOFTWARE\Khronos\OpenCL\Vendors] "<INST\_DIR>\windows\compiler\lib\intel64\_win\Intelocl64.dll"=dword:00000000 [HKEY\_LOCAL\_MACHINE\SOFTWARE\WOW6432Node\Khronos\OpenCL\Vendors] "<INST\_DIR>\windows\compiler\lib\ia32\_win\Intelocl32.dll"=dword:0000000

# 5.2 Manual Installation on Linux\* OS

- 1. Open a command prompt
- 2. Download the Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications (Linux version)
- 3. Create <TMP\_DIR> and change the current directory to it
- 4. Extract binaries from the package:

tar xzvf opencl\_runtime\_\*.tgz

rpm2cpio opencl\_runtime\_\*/rpm/intel-openclrt-18.1\*.rpm | cpio -idmv

- 5. Copy content of temporary directory to <TARGET\_DIR>
   cp -R <TMP\_DIR>/opt/intel/opencl\_\*/linux/compiler/lib/.
   <TARGET\_DIR>
- 6. Replace libOpenCL.so\* files available in the system with the ones from <TMP\_DIR>/opt/intel/opencl\_\*/linux/compiler/lib/intel64\_lin
- 7. If you would like to restore the initial files, preserve them in a temporary location.
- 8. Add <TARGET\_DIR>/intel64\_lin to the LD\_LIBRARY\_PATH environment variable.
- 9. Rename any Intel-related ICD files in the directory by adding suffix . hide
- 10. Create a new ICD file, for example:

echo <TARGET\_DIR>/intel64\_lin/libintelocl.so >
/etc/OpenCL/vendors/intel\_manual.icd

**NOTE:** Make sure that libOenCL.so\* libraries are available on the machine.

# 6 Known Issues

The following are known issues:

- Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications has a dependency on Intel<sup>®</sup> Threading Building Blocks (Intel<sup>®</sup> TBB) version 2017 Update 8 on Linux\* and 2018 Update 2 on Windows\* OS. The library is included in the Intel<sup>®</sup> CPU Runtime installation directory.
  - Linux\* OS case:

Make sure there is no other Threading Building Blocks (TBB) library in your OpenCL<sup>™</sup> host application library search path on Linux\* OS. Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications was tested only with Intel<sup>®</sup> Threading Building Blocks (Intel<sup>®</sup> TBB) libraries included in the package.

In case OpenCL<sup>™</sup> host application intentionally uses features of a standalone Threading Building Blocks (TBB) make sure that it is of a higher version than the library version in the package and is found earlier in the shared library search procedure. If standalone Threading Building Blocks (TBB) libraries are loaded functionality and performance may vary.

• Windows\* OS case:

In Windows\* OS product distribution Intel<sup>®</sup> Threading Building Blocks (Intel<sup>®</sup> TBB) libraries included in the package have different names rather than in standard shipment. That ensures that no load conflict takes place with pre-installed libraries.

In case OpenCL<sup>™</sup> host code uses features of a standalone Threading Building Blocks (TBB) libraries application performance may vary (e.g. due to oversubscription issues in case of both standalone library pool and OpenCL thread pool being fully loaded).

- After OpenCL<sup>™</sup> Contexts are created for both "Experimental OpenCL 2.1 CPU Only Platform" and "Intel(R) CPU Runtime for OpenCL(TM) Applications" OpenCL<sup>™</sup> Kernel compilation for any of the two may destabilize the application. Use "Intel(R) CPU Runtime for OpenCL(TM) Applications" platform for features of "Experimental OpenCL 2.1 CPU Only Platform" instead.
- Compilation of kernels with empty loop bodies may not terminate. Disable vectorization or all compiler optimizations in that case.
- Configuration file parameter CL\_CONFIG\_CPU\_TARGET\_ARCH cannot be set from cl.cfg, only as environment variable.

- The 'finish' GDB\* or LLDB\* command return to the same line as a function call. The 'finish' command should advance instruction pointer to the next instruction after the call. It is expected that it will point to the next line following the line of calling, except when a callee function returns a value for using. In the case of OpenCL<sup>™</sup> kernel code it points to the same line as the call, even the callee function is a void one.
- Setting <u>local</u> variable values within a debugger session ('set' GDB\* command) has no effect on program execution. An attempt to set variables in a local address space has no effect. The new value will be discarded just after the next step.
- Conditional breakpoints on work-items may take long time to evaluate. It may take time to evaluate conditional breakpoints depending on a work item number for heavy NDRange kernels. To avoid such breakpoints, the kernel source code can be modified using the 'get\_global\_id' function.
- Installing Intel<sup>®</sup> CPU Runtime for OpenCL<sup>™</sup> Applications 18.1 over an existing 16.1.2 deployment may result in an extra, non-usable CPU OpenCL<sup>™</sup> device still detected. Manually assisting uninstallation of 16.1.2 should remove the unintended platform and device detection:
  - 1. Remove following registry keys (see the system regedit utility):
    - On 32-bit OS:
      - HKEY\_LOCAL\_MACHINE\SOFTWARE\Khronos\OpenCL\Vendors \intelopencl32.dll
    - On 64-bit OS:
      - HKEY\_LOCAL\_MACHINE\SOFTWARE\Khronos\OpenCL\Vendors \intelopencl64.dll
      - HKEY\_LOCAL\_MACHINE\SOFTWARE\WOW6432Node\Khronos\ OpenCL\Vendors\intelopencl32.dll
  - 2. (optional) Remove following files if they exist:
    - On 32-bit OS:
      - C:\Windows\System32\intelopencl32.dll
    - On 64-bit OS:

- C:\Windows\System32\intelopencl64.dll
- C:\Windows\SysWOW64\intelopencl32.dll

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